

SUPPLEMENT OF HP-35 INSTRUCTION SET. (FEB. 20)

(HP-45 PROGRAM MODE INSTRUCTIONS)

TYPE 6.

TKR / TXR

x	x	x	1	0	1	0	0	0	0
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TRANSFER KEYCODE (EXTERNAL CODE) TO ROM ADDRESS.

TYPE 5.

LMD

x	x	0	1	1	1	0	0	0	0
---	---	---	---	---	---	---	---	---	---

LEARN MODE DISPLAY (OR-TIE KEYCODE INFORMATION ONTO IS LINE)

TYPE 8

LBR

0	0	0	1	0	0	0	0	0	0
---	---	---	---	---	---	---	---	---	---

LOAD MEMORY BUFFER TO ROM ADDRESS.

(NEXT MICROINSTRUCTION HAS TO BE NO-OP)

TYPE 9

MEMORY STACK OPERATION

F	1	0	0	0	0	0	0	0	0
---	---	---	---	---	---	---	---	---	---

F FIELD $\hat{=}$ OPERATION TYPE FIELD

{	F = 0 0	(MIN)	MEMORY INSERTION
	0 1	(MDE)	MEMORY DELETION
	1 0	(SRH)	SEARCH
	1 1	(CLM)	CLEAR MEMORY

TYPE 10

F	1	0	0	0	0	0	0	0	0
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F = 1	(PAD)	POINTER ADVANCE BY ONE
0	(MRK)	MARK AND SEARCH

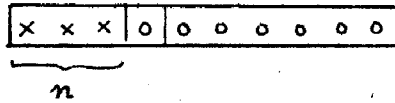
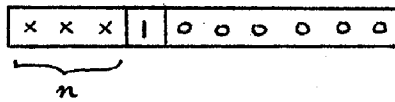
TYPE 11.

TAB.

1	1	1	1	0	0	0	0	0	0
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TRANSFER ROM ADDRESS TO MEMORY BUFFER.

INSTRUCTION USED IN P.S. CHIP FOR I/O FLG CONTROL

1 SET FLG # n  $n = 0, 1, 2, \dots, 7$ FLG # 2 = "1" $\Rightarrow$ C_1 = "1"FLG # 3 = "1" $\Rightarrow$ C_2 ENABLE2. INTERROGATE & RESET FLG # n THIS INSTRUCTION INTERROGATE FLG # n . IF FLG # n IS

SET THEN "FS" LINE ISSUES A PULSE WHICH IN TURN SET

"FLG" ON THE C&T CHIP. IF FLG # n IS "0", NOTHING HAPPENS.AFTER INTERROGATION, THE FLG # n IS ALWAYS RESET TO "0" STATE.

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